

CLAIMS

I Claim:

1. In an integrated circuit (IC) comprising a plurality of functional devices, a method for introducing a delay to signals on an interconnect connected to one of the functional devices, the method comprising applying a bias voltage to a first conductive line, wherein the first conductive line is parallel to the interconnect and is sized to couple a first predetermined capacitance to the interconnect, and wherein the first conductive line does not carry signals between any of the plurality of functional devices.

2. The method of Claim 1, further comprising applying the bias voltage to a second conductive line, wherein the second conductive line is parallel to the interconnect and is sized to couple a second predetermined capacitance to the interconnect, and wherein the second conductive line does not carry signals between any of the plurality of functional devices.

3. The method of Claim 2, wherein the first predetermined capacitance is different from the second predetermined capacitance.